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Attorney's Docket No.: 42390P10070

Patent

In re the Application of: Terry L. Kendall
(inventor(s))

**AMENDMENT UNDER
37 C.F.R. § 1.116
EXPEDITED PROCEDURE**

Application No.: 09/820,243

EXAMINING GROUP 2188

Filed: March 28, 2001

For: FLASH MEMORY LOW-LATENCY CACHE

(title)

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SIR: Transmitted herewith is an **Amendment After Final Action** for the above-referenced application.

 Applicant claims small entity status. See 37 CFR 1.27.

 X No additional fee is required.

 A Notice of Appeal is enclosed.

The fee has been calculated as shown below:

	(Col. 1)		(Col. 2)	(Col. 3)
	Claims Remaining After Amd.		Highest No. Previously Paid For	Present Extra
Total Claims	* 11	Minus	** 20	0
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<input type="checkbox"/>	First Presentation of Multiple Dependent Claim(s)			

* If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.

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SMALL ENTITY	
Rate	Additional Fee
X9	\$
X43	\$
+145	\$
Total Add. Fee	\$

OTHER THAN A SMALL ENTITY	
Rate	Additional Fee
X18	\$ 0
X86	\$ 0
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Date: 1/20/2004

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